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1. **A 145 MHz user-programmable gate array**
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6. **A dedicated digital unit for event recognition and centroiding in photon counting CCDs**
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	(US-20040088150-$).did. or (US-6188975-$ or US-5870588-$ or US-5937185-$ or US-3891974-$ or US-5594890-$ or US-6057706-$).did.	US-PGPUB; USPAT	OR	OFF	2005/06/13 11:35
S1	12	(hardware with software with (co-verification coverification)).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:22
S3	3	("6321366" "6810442" "6754763").pn.	USPAT	OR	OFF	2005/06/03 10:45
S4	843	(Swoboda).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 10:46
S5	29	(profit).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 10:47
S6	25	"703".clas. and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 10:58
S7	1	"716".clas. and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 10:59
S8	16	(FPGA PLD (reconfigurable with logic)) and (DMA) and (testbench)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:04
S9	149	(EDA) and DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:05
S10	68	(hardware with software with (co-verification coverification))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S11	10	("5493723" "6356862" "6223272" "6223144" "5329471" "5546562" "6263302" "6188975" "6332201" "6032268").pn.	USPAT	OR	OFF	2005/06/03 11:23
S12	4	("5911059" "6173419").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:40
S14	8	("5663900" "6161199" "6347395" "6009270" "6185522" "6223144" "6173419" "6516428").pn.	USPAT	OR	OFF	2005/06/03 11:44

S15	5	("6202044" "5748875" "5978584" "6356862" "6718294").pn.	USPAT	OR	OFF	2005/06/03 11:48
S16	3	("5748875" "6202044" "5493723").pn.	USPAT	OR	OFF	2005/06/03 11:49
S17	0	(EDA) same ((internal with bus) with (PLA PLD FPGA MCM))	USPAT	OR	OFF	2005/06/06 15:04
S18	27	(EDA) with (PLA PLD FPGA MCM)	USPAT	OR	OFF	2005/06/03 11:58
S19	0	"09/954715"	USPAT	OR	OFF	2005/06/03 11:58
S20	0	"09/954715"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:58
S21	1	"09918600"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:39
S22	0	EDA same PCI same DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:00
S23	14	EDA same PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:00
S24	107	(hardware with software with (co-simulation cosimulation))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:07
S25	2	"6009256".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:26

S26	128	("3106698" | "3287702" | "3287703" | "3473160" | "4020469" | "4306286" | "4386403" | "4488354" | "4503386" | "4541071" | "4577276" | "4578761" | "4593363" | "4612618" | "4621339" | "4642487" | "4656580" | "4656592" | "4675832" | "4682440" | "4695999" | "4697241" | "4700187" | "4706216" | "4736338" | "4740919" | "4744084" | "4747102" | "4752887" | "4758985" | "4768196" | "4777606" | "4786904" | "4787061" | "4791602" | "4803636" | "4811214" | "4815003" | "4823276" | "4827427" | "4835705" | "4849904" | "4849928" | "4862347" | "4870302" | "4872125" | "4876466" | "4882690" | "4901259" | "4901260" | "4908772" | "4914612" | "4918440" | "4918594" | "4922432" | "4924429" | "4931946" | "4935734" | "4942536" | "4942615" | "4945503" | "4949275" | "4951220" | "4965739" | "5003487" | "5023775" | "5036473" | "5041986" | "5046017" | "5051938" | "5053980" | "5081602" | "5084824" | "5093920" | "5109353" | "5114353" | "5126966" | "5128871" | "5140526" | "5146460" | "5189628" | "5193068" | "5197016" | "5224056" | "5231588" | "5231589" | "5233539" | "5253181" | "5258932" | "5259006" | "5260881" | "5263149" | "5272651" | "5329470" | "5343406" | "5352123" | "5371390" | "5377124" | "5425036" | "5448496" | "5448522" | "5452227" | "5452231" | "5452239" | "5467462" | "5475830" | "5477475" | "5504354" | "5563829" | "5612891" | "5644515" | "5649167" | "5649176" | "5654564" | "5657241" | "5659716" | "5661409" | "5661662" | "5761097" | "5796623" | "5841967" | "5867541" | "6009256" | "6009531").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:16
S27	20	(US-5937179-$ or US-5684721-$ or US-5329471-$ or US-6691268-$ or US-6836877-$ or US-5546562-$ or US-6356862-$ or US-6223144-$ or US-6188975-$ or US-5663900-$ or US-6202044-$ or US-6286114-$ or US-6182247-$ or US-6212489-$ or US-6052524-$ or US-6075935-$ or US-6209120-$).did. or (US-6212489-$ or US-6188975-$ or US-6009256-$).did.	USPAT; DERWENT	OR	OFF	2005/06/03 15:44
S28	11	S27 and (eda and model and bus (reconfig$ FPGA PLD) and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:34
S29	1	S28 and DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:21
S30	4	S27 and DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:21
S31	1	S28 and (shared adj memory)	USPAT; DERWENT	OR	OFF	2005/06/03 14:30
S32	2	S27 and (eda and model and bus and (reconfig$ FPGA PLD) and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:34
S33	9	S27 not S28	USPAT; DERWENT	OR	OFF	2005/06/03 14:44
S35	1	(behavioral with (RTL gate)).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:45

S36	32	(behavioral with synthesi$6).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:50
S37	0	"7564951".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:50
S38	2	"5764951".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:50
S39	8	("4827427" | "5111413" | "5128871" | "5237513" | "5274793" | "5437037" | "5544066" | "5572437").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:50
S40	31	("4353117" | "4587625" | "4635208" | "4675832" | "4697241" | "4703435" | "4789944" | "4805113" | "4813013" | "4827427" | "4831543" | "4833619" | "4890238" | "4908772" | "4918614" | "4922432" | "4965741" | "4967367" | "4970664" | "5005136" | "5034899" | "5084824" | "5111413" | "5164908" | "5164911" | "5220512" | "5222030" | "T940008" | "T940020").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:51
S41	10	eda and ((shar$3 adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:54
S42	10	eda and (((shar$3 common central host) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:54
S43	10	eda and (((shar$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:55
S44	0	(hardware with design with tool) and (((shar$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:56
S45	10	(design with tool) and (((shar$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:56
S46	127	((shared common host joint target) with memory) and (hardware with software) and (co-verification coverification cosimulation co-simulation)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:57
S47	2	"5838948".pn.	USPAT; DERWENT	OR	OFF	2005/06/03 15:48
S48	75	eda and control$4 and DMA	USPAT; DERWENT	OR	OFF	2005/06/06 11:36
S49	3	("5815688" | "5838948" | "6134516").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 15:50
S50	31	("4835736" | "5058114" | "5568437" | "5572712" | "5640542" | "5717699" | "5764079" | "5821771" | "5870410" | "5960191" | "6014334" | "6016563" | "6020758" | "6157210" | "6182247").PN. OR ("6286114").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 16:40
S51	0	(FPGA PLD (reconfigurable adj logic)) same memory same (hardware with software) same (cosimualtion co-simulat$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:52

S52	0	(FPGA PLD PLA) same memory same (hardware with software) same (cosimualtion co-simulat$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:53
S53	1124	(FPGA PLD PLA) same memory same (hardware with software)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:53
S54	155	(FPGA PLD PLA) with memory with (hardware with software)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:53
S55	8	("5889685" "6370675" "6083269" "5838947").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 17:00
S56	4	("5889685" "6370675" "6083269" "5838947").pn.	USPAT	OR	OFF	2005/06/03 17:00
S57	137	quickturn.as.	USPAT; DERWENT	OR	OFF	2005/06/06 14:53
S58	0	S57 and DMA	USPAT; DERWENT	OR	OFF	2005/06/06 11:37
S59	0	S57 and (direct with memory with access)	USPAT; DERWENT	OR	OFF	2005/06/06 11:38
S60	4	S57 and (shared with memory)	USPAT; DERWENT	OR	OFF	2005/06/06 11:39
S61	52	S57 and (memory with access)	USPAT; DERWENT	OR	OFF	2005/06/06 11:39
S62	3	S60 and S61	USPAT; DERWENT	OR	OFF	2005/06/06 11:39
S63	25	(US-5937179-$ or US-5684721-$ or US-5329471-$ or US-6691268-$ or US-6836877-$ or US-5546562-$ or US-6356862-$ or US-6223144-$ or US-6188975-$ or US-5663900-$ or US-6202044-$ or US-6286114-$ or US-6182247-$ or US-6212489-$ or US-6052524-$ or US-6075935-$ or US-6209120-$ or US-5572437-$ or US-6298320-$ or US-6108494-$ or US-5838948-$ or US-5815688-$ or US-5968161-$ or US-5661662-$).did. or (US-6286128-$).did.	USPAT; DERWENT	OR	OFF	2005/06/07 14:06
S64	3	S63 and (bus) and (shared with memory)	USPAT; DERWENT	OR	OFF	2005/06/06 14:24
S65	76	(bus) and (shared with memory) and (eda (electronic with design with automation))	USPAT; DERWENT	OR	OFF	2005/06/06 14:24
S66	8	S63 and (state with machine)	USPAT; DERWENT	OR	OFF	2005/06/06 14:44
S67	72	quickturn.as.	USPAT	OR	OFF	2005/06/06 14:53
S68	145	((internal with bus) with (reconfigurable PLA PLD FPGA MCM))	USPAT	OR	OFF	2005/06/06 15:19
S69	15	S68 and ((shar$4 with memory) and DMA)	USPAT	OR	OFF	2005/06/06 15:20
S70	3	("5197130" | "5784630" | "6167502").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/06 15:23

S71	180	("3106698" | "3287702" | "3287703" | "3473160" | "3810577" | "3928730" | "3955180" | "4020469" | "4032899" | "4306286" | "4315315" | "4357678" | "4386403" | "4404635" | "4459694" | "4488354" | "4503386" | "4510602" | "4524240" | "4525789" | "4527115" | "4527249" | "4539564" | "4541071" | "4577276" | "4578761" | "4583169" | "4587625" | "4593363" | "4600846" | "4612618" | "4613940" | "4621339" | "4642487" | "4656580" | "4656592" | "4674089" | "4675832" | "4695740" | "4695950" | "4695968" | "4695999" | "4697241" | "4700187" | "4706216" | "4713557" | "4722084" | "4725835" | "4725971" | "4736338" | "4740919" | "4744084" | "4747102" | "4752887" | "4758745" | "4758985" | "4761768" | "4766569" | "4768196" | "4769817" | "4777606" | "4782440" | "4782461" | "4786904" | "4787061" | "4787062" | "4791602" | "4803636" | "4811214" | "4815003" | "4819150" | "4823276" | "4827427" | "4829202" | "4835705" | "4845633" | "4849904" | "4849928" | "4854039" | "4855669" | "4862347" | "4864165" | "4868419" | "4870302" | "4873459" | "4876466" | "4879646" | "4882690" | "4899273" | "4901259" | "4901260" | "4908772" | "4914612" | "4918440" | "4918594" | "4922432" | "4924429" | "4931946" | "4935734" | "4937827" | "4942536" | "4942615" | "4945503" | "4949275" | "4951220" | "4958324" | "4965739" | "4972334" | "4972372" | "5003487" | "5023775" | "5031129" | "5036473" | "5041986" | "5046017" | "5051938" | "5053980" | "5068812" | "5081602" | "5083083" | "5084824" | "5093920" | "5109353" | "5114353" | "5126966").PN. OR ("5128871" | "5140526" | "5172011" | "5224055" | "5224056" | "5231588" | "5231589" | "5233539" | "5253363" | "5259006" | "5276854" | "5321828" | "5329470" | "5329471" | "5331571" | "5339262" | "5341483" | "5345580" | "5352123" | "5377123" | "5386550" | "5396498" | "5425036" | "5437037" | "5448496" | "5448522" | "5452231" | "5452239" | "5475624" | "5475830" | "5477475" | "5479355" | "5530958" | "5544069" | "5546562" | "5551013" | "5563829" | "5572710" | "5574388" | "5596742" | "5612891" | "5623664" | "5649176" | "5659716").PN. OR ("5970240").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/06 15:27
S72	2	09/900124	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 15:50
S73	840	(DMA with PCI) and (emulat$4 simulat$4 model$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 16:42
S74	37	(DMA with PCI with (reconfig$6 FPGA PLD PLA MCM)) and (emulat$4 simulat$4 model$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 16:52
S75	177	(DMA with (reconfig$6 FPGA PLD PLA MCM)) and (emulat$4 simulat$4 model$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 16:52
S76	140	S75 not S74	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 17:52
S77	1	"6321366".pn.	USPAT	OR	OFF	2005/06/06 17:56

S81	1	US20020152060$A	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 18:18
S82	4	("5838948" "5815688").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 18:18
S83	1	"5450551".pn.	USPAT	OR	OFF	2005/06/06 19:53
S84	28	(US-5937179-$ or US-5684721-$ or US-5329471-$ or US-6691268-$ or US-6836877-$ or US-5546562-$ or US-6356862-$ or US-6223144-$ or US-6188975-$ or US-5663900-$ or US-6202044-$ or US-6286114-$ or US-6182247-$ or US-6212489-$ or US-6052524-$ or US-6075935-$ or US-6209120-$ or US-5572437-$ or US-6298320-$ or US-6108494-$ or US-5838948-$ or US-5815688-$ or US-5968161-$ or US-5661662-$ or US-5970240-$ or US-6094532-$).did. or (US-6286128-$ or EP-453171-$).did.	USPAT; DERWENT	OR	OFF	2005/06/08 16:51
S85	9	S84 and (plural$5 and reconfig$8)	USPAT; DERWENT	OR	OFF	2005/06/07 14:48
S86	1105	FPGA with interconnect$4	USPAT; DERWENT	OR	OFF	2005/06/07 15:06
S87	9	S86 and (hyper-cube hypercube)	USPAT; DERWENT	OR	OFF	2005/06/07 15:07
S88	136	series adj FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:27
S89	0	(series adj interconnect$4) with FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:27
S90	21	(series with interconnect$4) with FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:28
S91	22	daisy with chain with FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:33
S92	165	703/20.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:45
S93	1926	719/312,313,318,319,310.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:47

S94	4	S93 and unix and (verilog vhdl (hardware adj descript$6 adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:49
S95	10	S93 and (verilog vhdl (hardware adj descript$6 adj language)) and (simulat$4 emulat$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:52
S96	15	S93 and (verilog vhdl (hardware adj descript$6 adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:51
S97	330	S93 and (simulat$4 emulat$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 18:13
S98	2	"5109353".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 18:13
S99	10	software with hardware with pointer with mapping	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:01
S100	1129	hardware with software with mapp$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:01
S101	349	S100 and pointer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:01
S102	339	S101 not S99	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:04
S103	40	(control with logic) and ((data-in with pointer with logic) or (data-in with latch with logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:06
S104	9	(control with logic) and ((data-in with pointer with logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:07

S105	4	(pointer with based) same (hardware with software with mapping)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:08
S106	17	(pointer) same (hardware with software with mapping)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 16:50
S107	28	(US-5937179-$ or US-5684721-$ or US-5329471-$ or US-6691268-$ or US-6836877-$ or US-5546562-$ or US-6356862-$ or US-6223144-$ or US-6188975-$ or US-5663900-$ or US-6202044-$ or US-6286114-$ or US-6182247-$ or US-6212489-$ or US-6052524-$ or US-6075935-$ or US-6209120-$ or US-5572437-$ or US-6298320-$ or US-6108494-$ or US-5838948-$ or US-5815688-$ or US-5968161-$ or US-5661662-$ or US-5970240-$ or US-6094532-$).did. or (US-6286128-$ or EP-453171-$).did.	USPAT; DERWENT	OR	OFF	2005/06/08 16:51
S108	6	S107 and pointer	USPAT; DERWENT	OR	OFF	2005/06/08 16:54
S109	215	hardware with software with pointer	USPAT; DERWENT	OR	OFF	2005/06/08 16:54
S110	18	S109 with (simulat$4 emulat$4 model$6)	USPAT; DERWENT	OR	OFF	2005/06/08 19:47
S111	1	"5867541".pn.	USPAT	OR	OFF	2005/06/08 19:53
S112	4	("5649176" "5659716" "5761097" "6009531").pn.	USPAT	OR	OFF	2005/06/08 19:54
S113	1072	delay with insensitive	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:18
S115	476	delay adj3 insensitive	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:18
S116	58	(delay adj3 insensitive) with asynchronous	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:23
S117	41	(timing with insensitive) same (trigger latch)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:25
S118	501	(timing with insensitive)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:25
S119	17	S118 and ((glitch adj2 free) or glitchless)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:28
S120	510	(glitch near free) and (trigger or latch or flip$flop)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:28
S121	521	(glitch with free) and (trigger or latch or flip$flop)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:30
S122	218	(glitch with free) same (trigger or latch or flip$flop)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:46
S123	10	((gated or derived) near clock) same ((glitch adj less) or glitchfree or (glitch adj free) or glitchless)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:49

S124	9	FPGA same (external adj I/O) same simulat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:32
S126	9	reconfig$6 same (external adj I/O) same simulat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:32
S127	9	(PLA PLD FPGA MCM) same (external adj I/O) same simulat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S128	0	(PLA PLD FPGA MCM) same (external adj device) same simulat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S129	0	FPGA same (external adj device) same simulat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S130	0	FPGA same (external adj device) same (simulat$4 emaulat$4 model$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S131	9	FPGA same (external adj I/O) same (emaulat$4 model$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:34
S132	48	FPGA same (external adj I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:34
S133	39	S132 not S126	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:38
S135	138	(external with I/O) with (simulat$4 emaulat$4 model$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:39
S137	129	S135 not S132	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:51

S138	1	read write multiple FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2005/06/09 15:52
S139	0	(multiple with FPGA) same (access interface I/O read write)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2005/06/09 15:52
S140	274	(multiple with FPGA) same (access interface I/O read write)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:52
S141	13	("5497498" | "5535342" | "5539330" | "5583749" | "5603043" | "5628028" | "5784636" | "5788669" | "5794062" | "5991822" | "5999990" | "6185484" | "6230307").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 16:04
S142	21	bus with selector with FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 16:10
S143	175	bus with selector and FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:54
S144	184	703/28.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:55
S145	331	703/21.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:55
S146	18	S145 and FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:57
S147	537	703/26,19.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:57
S148	65	S147 and (FPGA reconfig$6 PLA PLD MCM)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:58
S150	40	(US-6169422-$ or US-5387825-$ or US-5808486-$ or US-5539330-$ or US-5109353-$ or US-5329471-$ or US-5363501-$ or US-5546562-$ or US-5572437-$ or US-5661662-$ or US-5663900-$ or US-5684721-$ or US-5815688-$ or US-5838948-$ or US-5905883-$ or US-5911059-$ or US-5937179-$ or US-5968161-$ or US-5970240-$ or US-6052524-$ or US-6075935-$ or US-6094532-$ or US-6108494-$ or US-6182247-$ or US-6188975-$ or US-6202044-$).did. or (US-6209120-$ or US-6212489-$ or US-6223144-$ or US-6286114-$ or US-6298320-$ or US-6356862-$ or US-6691268-$ or US-6766284-$ or US-6836877-$ or US-5535342-$ or US-5603043-$ or US-6304903-$).did. or (EP-453171-$ or US-6286128-$).did.	USPAT; DERWENT	OR	OFF	2005/06/10 14:14

S151	40	(US-6169422-$ or US-5387825-$ or US-5808486-$ or US-5539330-$ or US-5109353-$ or US-5329471-$ or US-5363501-$ or US-5546562-$ or US-5572437-$ or US-5661662-$ or US-5663900-$ or US-5684721-$ or US-5815688-$ or US-5838948-$ or US-5905883-$ or US-5911059-$ or US-5937179-$ or US-5968161-$ or US-5970240-$ or US-6052524-$ or US-6075935-$ or US-6094532-$ or US-6108494-$ or US-6182247-$ or US-6188975-$ or US-6202044-$).did. or (US-6209120-$ or US-6212489-$ or US-6223144-$ or US-6286114-$ or US-6298320-$ or US-6356862-$ or US-6691268-$ or US-6766284-$ or US-6836877-$ or US-5535342-$ or US-5603043-$ or US-6304903-$).did. or (EP-453171-$ or US-6286128-$).did.	USPAT; DERWENT	OR	OFF	2005/06/10 14:14
S152	21	S151 and (behavior$3)	USPAT; DERWENT	OR	OFF	2005/06/10 14:40
S153	27	S151 and (condition$4 if-then)	USPAT; DERWENT	OR	OFF	2005/06/10 14:51
S154	27	S151 and (condition$4 "if-then")	USPAT; DERWENT	OR	OFF	2005/06/10 14:52
S158	2	S151 and ("if-then"$)	USPAT; DERWENT	OR	OFF	2005/06/10 14:53
S159	134	("if-then"$) and (emulat$4)	USPAT; DERWENT	OR	OFF	2005/06/10 17:01
S160	6594	((emulat$4 hardware) with interrupt$4) and ((software simulat$4))	USPAT; DERWENT	OR	OFF	2005/06/10 17:02
S161	4885	((emulat$4 hardware) with interrupt$4) and ((software simulat$4) with interrupt$4)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:14
S162	892	(emulat$4 simulat$4) and (hardware with interrupt$5 with software)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 13:54
S163	12	(emulat$4 simulat$4) and (hardware with initiated with interrupt$5 with software)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 12:15
S164	6	"if-then"$ with interrupt$6	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:12
S165	3	"09/918600"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:44
S166	88	(PLD FPGA PLA reconfig$9) with hardware with interrupt$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:44
S167	18	("3891974" | "5274831" | "5511217" | "5884023" | "5960191" | "6055649" | "6075941" | "6085336" | "6173386" | "6289300" | "6370606" | "6385742" | "6385747" | "6446221" | "6522985" | "6564339" | "6567933" | "6681341").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:03

S168	2656	emulat$ with (interrupt$6 condition$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:07
S170	305	S168 and (FPGA PLA PLD)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:04
S171	3	emulat$ with (interrupt$6) with ("test-bench" testbench)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:07
S172	3	emulat$ with (interrupt$6) with ("test-rig" simulator)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:10
S173	15	emulat$ with (interrupt$6) with (supend wait resume)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:10
S174	7	("5103394" | "5666519" | "5678028" | "5737579" | "5761477" | "5815688" | "6047381").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:11
S175	9	("4638423" | "5077657" | "5459872" | "5493672" | "5546562" | "5551013" | "5572710" | "5574927" | "5600579").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:20
S176	12	(hardware with interrupt near software) same emulat$4	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:20
S177	3	("5737516" | "5828824" | "6324684").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:29
S178	1442	FPGA and debug$5	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:30
S179	176	FPGA with debug$5	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:30
S180	68	(hardware with software with (co-verification coverification))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S181	107	(hardware with software with (co-simulation cosimulation))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S182	16	S180 and S181	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S183	159	S180 or S181	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S184	79	S183 and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:38

S185	35	(emulat$4 simulat$4) and ((hardware with breakpoint$5) near software)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 13:54
S186	2	hardware near interrupt$4 near service near software	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/12 14:42
S187	0	hardware adj interrupt$4 adj service adj software	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/12 14:42
S188	279	hardware with interrupt$4 with service with software	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/12 14:43
S189	0	("2004/0088150").URPN.	USPAT	OR	OFF	2005/06/12 14:45
S190	47	(US-20040088150-$).did. or (US-5109353-$ or US-5329471-$ or US-5363501-$ or US-5387825-$ or US-5535342-$ or US-5539330-$ or US-5546562-$ or US-5572437-$ or US-5603043-$ or US-5661662-$ or US-5663900-$ or US-5684721-$ or US-5808486-$ or US-5815688-$ or US-5838948-$ or US-5905883-$ or US-5911059-$ or US-5937179-$ or US-5968161-$ or US-5970240-$ or US-6052524-$ or US-6075935-$ or US-6094532-$ or US-6108494-$ or US-6169422-$ or US-6182247-$).did. or (US-6188975-$ or US-6202044-$ or US-6209120-$ or US-6212489-$ or US-6223144-$ or US-6286114-$ or US-6298320-$ or US-6304903-$ or US-6356862-$ or US-6691268-$ or US-6766284-$ or US-6836877-$ or US-5600579-$ or US-5870588-$ or US-5937185-$ or US-3891974-$ or US-5594890-$ or US-6057706-$).did. or (EP-453171-$ or US-6286128-$).did.	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/06/12 14:52
S191	26	S190 and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 15:01
S192	280	hardware with behavior with process$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 15:03
S193	112	S192 and emulat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 15:03
S195	2	"if-then-else"$ with interrupt$6	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:13

S196	16	"if-then-else"$ with hardware	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:15
S197	1	"if-then-else"$ with emulat$	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:15
S198	38	("4638423" | "5077657" | "5459872" | "5493672" | "5546562" | "5551013" | "5572710" | "5574927" | "5600579").PN. OR ("5838948").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 15:35
S199	1	"5815688".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 15:53
S200	513	(FPGA hardware) with implement$ with interrupt	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 15:54
S201	41	buza.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/13 09:55
S202	7	bunza.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/13 09:55